

PTO/SB/08A (08-03)

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~~Substitute for form 1449/PTO~~

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 1 of 2

INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>		Complete if Known	
Sheet	of	2	Attorney Docket Number
		10/776,475	
		2/10/2004	
		Gunn III, et al	
		Not Yet Assigned	
		LUX - P019	

U. S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

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Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T
		Country Code ² -Number ⁴ -Kind Code ³ (if known)				
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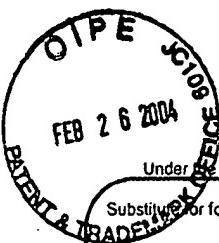
Examiner Signature	Daniel Pethouse	Date Considered	11/16/05
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Application Number	10/776,475
Filing Date	2/10/2004
First Named Inventor	Gunn III, et al
Art Unit	Not Yet Assigned
Examiner Name	
Attorney Docket Number	LUX - P019

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
DGP	1	"LOW LOSS NANOPHOTONIC WAVEGUIDES AND RING RESONATORS IN SILICON-ON-INSULATOR"; BEATS, ET AL	
DGP	2	"LARGE-SCALE PRODUCTION TECHNIQUES FOR PHOTONIC NANOSTRUCTURES"; BOGAERTS, ET AL	
DGP	3	"CMOS-COMPATIBLE PLANAR SILICON WAVEGUIDE-GRATING-COUPLER PHOTODETECTORS FABRICATED ON SILICON-ON-INSULATOR (SOI) SUBSTRATES"; S.M. CSUTAK, S. DAKSHINA-MURTHY &	
DGP		JOE C. CAMPBELL; IEEE JOURNAL OF QUANTUM ELECTRONICS, VOL.38 NO. 5 MAY 2002	
DGP	4.	"BURIED-OXIDE SILICON-ON-INSULATOR STRUCTURES II: WAVEGUIDE GRATING COUPLERS"; EMMONS, ET AL; IEEE JOURNAL OF QUANTUM ELECTRONICS, VOL.28 NO. 1 JANUARY 1992.	
DGP	5	"INTEGRATED OPTICS FOR OPTICAL HEAD APPLICATIONS"; OCHIAI ET AL; ELECTRONICS LETTERS 18TH AUGUST 1994 VOL. 30 NO. 17, PAGE 1403-1404	
DGP	6	"EFFECTS OF GRATING HEIGHTS ON HIGHLY EFFICIENT UNIBOND SOI WAVEGUIDE GRATING COUPLERS"; ANG, ET AL; IEEE PHOTONICS TECHNOLOGY LETTERS VOL.12 NO. 1 JAN 2000	
DGP	7	"MODE MATCHING TECHNIQUE FOR HIGHLY EFFICIENT COUPLING BETWEEN DIELECTRIC WAVEGUIDES AND PLANAR PHOTONIC CRYSTAL CIRCUITS"; SANCHIS, ET AL	
DGP	8	"BURIED-OXIDE SILICON-ON-INSULATOR STRUCTURES II: WAVEGUIDE GRATING COUPLERS"; EMMONS, ET AL; IEEE JOURNAL OF QUANTUM ELECTRONICS, VOL 28 NO.1 JANUARY 1992	
DGP	9	"NON UNIFORM GRATING COUPLERS FOR COUPLING OF GAUSSIAN BEAMS TO COMPACT WAVEGUIDES"; WEST, ET AL; PUBLISHED IN THE INTEGRATED PHOTONICS RESEARCH TECHNICAL DIGEST 1994	

Examiner Signature	Daniel Patterson	Date Considered	11/16/05
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>		Application Number	10/776,475
		Filing Date	2/10/2004
		First Named Inventor	Gunn et al.
		Art Unit	2817
		Examiner Name	(not yet assigned)
Sheet	1	of	12
		Attorney Docket Number	LUX-P019

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